

Serial No. 09/583,171
September 23, 2003
Reply to the Office Action dated April 30, 2003
Page 2 of 6

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

Claims 1-8 (canceled)

Claim 9 (currently amended): A delay line comprising:
a multilayer structure formed by laminating a plurality of dielectric layers;
a transmission line formed on a dielectric layer embedded in the multilayer structure;
a plurality of ground conductors disposed on the dielectric layers and a pair of said ground conductors being disposed on opposite sides of the transmission line; and
a capacitance disposed on the multilayer structure and connected in parallel to the transmission line for setting a desired delay time of the delay line, wherein said capacitance is adjustable.

Claim 10 (original): A delay line according to claim 9, wherein said capacitance is provided by electrodes formed on respective ones of said dielectric layers.

Claim 11 (original): A delay line according to claim 9, wherein said capacitance is provided by a variable capacitor.

Claim 12 (original): A delay line according to claim 9, wherein said capacitance is provided by a varicap diode.

Claim 13 (currently amended): A delay line comprising:
a multilayer structure formed by laminating a plurality of dielectric layers;

Serial No. 09/583,171
September 23, 2003
Reply to the Office Action dated April 30, 2003
Page 3 of 6

a transmission line formed on a dielectric layer embedded in the multilayer structure;

a plurality of ground conductors disposed on the dielectric layers and a pair of said ground conductors being disposed on opposite sides of the transmission line; and

a capacitance disposed on the multilayer structure and connected in parallel to the transmission line for setting a desired delay time of the delay line, wherein said capacitance is provided by a diode.

Claim 14 (original): A delay line according to claim 13, wherein said diode is a varicap diode.

Claim 15 (canceled)

Claim 16 (previously presented): A delay line according to claim 9, wherein said capacitance is connected in parallel to the transmission line.

Claim 17 (previously presented): A delay line according to claim 13, wherein said capacitance is connected in parallel to the transmission line.